ABSTRACT OF THE DISCLOSURE

The processor of a receiving device does not execute acquisition and release of resources of a band and a channel for the isochronous data transfer every time the processor receives data from the sending device by isochronous transfer, but executes the acquisition of the resources of the band and the channel for the isochronous data transfer via the bus and an I/F board only when the completion of a bus reset is detected and holds the resources until the next bus reset is caused. By this processing, the receiving device can consistently secure the resources necessary for the isochronous data transfer to the sending device so long as the resources acquisition is successfully achieved when the bus reset is completed. This therefore enables the prevention of the occurrence of a situation in which the receiving device becomes unable to receive the data from the sending device by the isochronous transfer due to a resource shortage caused by having failed in releasing the resources when the receiving device stopped the isochronous data transfer previously.